Department of Electrical Engineering

School of Electrical and Computer Sciences, IIT Bhubaneswar

List of candidates shortlisted for technical interview round towards admission into MS by Research Programme in Electrical Engineering Discipline for Spring 2025-26 academic session

Interview	Venue
Date of Interview: 05th December 2025 (Friday) Reporting time for document verification: 9 AM Interviews start after document verification	School of Electrical and Computer Sciences IIT Bhubaneswar, Argul, Odisha, Pin 752050

List of shortlisted candidates						
Sl. No.	Application no.	Name of Applicant	Gender	Category	School	Department
1	2025210410072	ANSHUMAN DIXIT	male	GE	SECS	EE

Head of Department
Department of Electrical Engineering