

# Department of Electrical Engineering

## School of Electrical and Computer Sciences, IIT Bhubaneswar

### List of candidates shortlisted for technical interview round towards admission into PhD Programme in Electrical Engineering Discipline for Spring 2025-26 academic session

Interview	Venue
Date of Interview: 05 <sup>th</sup> December 2025 (Friday) Reporting time for document verification: 9 AM Interviews start after document verification	School of Electrical and Computer Sciences IIT Bhubaneswar, Argul, Odisha, Pin 752050

List of shortlisted candidates						
Sl. No.	Application no.	Name of Applicant	Gender	Category	School	Department
1	2025201110130	MANASI PATRO	female	GE	SECS	EE
2	2025201110332	PRAGYA RAJ	female	OB	SECS	EE
3	2025201110665	JINMAYEE PADHY	female	GE	SECS	EE
4	2025201110757	SHEETAL KONDEL	female	GE	SECS	EE
5	2025201111012	DEVANSH TRIPATHI	male	GE	SECS	EE
6	2025201210657	SATISH KUMAR PATNAIK	male	GE	SECS	EE
7	2025201210693	SUNANTHA A	female	GE	SECS	EE
8	2025201211204	BODA BIXAM	male	ST	SECS	EE
9	2025201211493	NISHANT KUMAR	male	OB	SECS	EE
10	2025201310284	GAUTAM SINGH	male	OB	SECS	EE
11	2025201310675	JYOTI VERMA	female	OB	SECS	EE
12	2025201410127	SHANTANU KUMAR SINGH	male	GE	SECS	EE
13	2025201510459	LIPSA PANI	female	GE	SECS	EE
14	2025201610478	SHIVENDRA BHUSHAN	male	GE	SECS	EE
15	2025201611295	AMALINA DAS	female	GE	SECS	EE
16	2025201611409	BIGTORAJ DEBBARMA	male	ST	SECS	EE
17	2025201710245	GOVIND KUMAR MAURYA	male	OB	SECS	EE
18	2025201710982	RITESH KUMAR SINGH	male	GE	SECS	EE

Head of Department  
Department of Electrical Engineering