भारतीय प्रौद्योगिकी संस्थान भुवनेश्वर
Indian Institute of Technology Bhubaneswar

Media/Publication	The Times of India		
Date	5th August 2026	Language	English
Headline	IIT inks pact with semiconductor co. to offer students industry exposure		
Link	https://timesofindia.indiatimes.com/city/bhubaneswar/iit-inks-pact-with-semiconductor-co-to-offer-students-industry-exposure/articleshow/132929089.cms		

IIT Bhubaneswar and Banashree Semiconductors representative during signing of the MoU

Bhubaneswar: IIT Bhubaneswar has signed a five-year memorandum of understanding (MoU) with a semiconductor technology company to promote joint research, industry-focused education, training and innovation in the field.

According to an official statement issued by the institute on Wednesday, the collaboration aims to strengthen the link between academia and industry in the semiconductor sector. The company, Banashree Semiconductors, works in advanced silicon-node technologies, low-power chip design, and optimisation of power, performance and area, with emphasis on reduced transistor-count architectures for system-on-chip applications.

The partnership will combine IIT Bhubaneswar's academic and research strengths with Banashree Semiconductors' industry expertise. As part of the agreement, both organisations will take up joint research and development projects, create design optimisation flows, and support faculty and student exchanges, internships, training programmes and collaborative research activities.

The two sides will also explore externally funded research and consultancy projects.

They may share access to computing resources, design tools, laboratory facilities, testing infrastructure and research equipment.

A major component of the collaboration will be practical industry exposure for students. The IIT Bhubaneswar students will get opportunities to pursue internships, project work, training and research at Banashree Semiconductors. Engineers and scientists from the company may also participate in research and academic programmes at the institute.

The agreement also includes plans for joint workshops, seminars, short-term courses, conferences and research publications.

The initiative is expected to support skill development and promote industry-relevant research in semiconductor design and related areas. It will also help bridge the gap between academic research and the needs of the semiconductor industry.

IIT Bhubaneswar had received administrative approval from ministry of electronics and information technology in 2022 to implement the Chips to Start-up Programme, which is part of the Centre's efforts to position India as a major semiconductor hub.

Another semiconductor company, SiCSem, has also set up a research and innovation centre at IIT Bhubaneswar with an investment of Rs 64 crore. The institute has partnered with other companies for research in the sector and has introduced an MTech programme in semiconductor technology and chip design to meet growing industry demand.

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Media/Publication	OrissaDiary.com		
Date	5th August 2026	Language	English
Headline	IIT Bhubaneswar and Banashree Semiconductors Join Hands to Advance Semiconductor Research, Education and Industry Collaboration		
Link	https://orissadiary.com/iit-bhubaneswar-and-banashree-semiconductors-join-hands-to-advance-semiconductor-research-education-and-industry-collaboration/		

In a move to strengthen academia–industry collaboration in the semiconductor sector, Indian Institute of Technology (IIT) Bhubaneswar has recently signed a Memorandum of Understanding (MoU) with Banashree Semiconductors for joint research, industry-oriented education, training and innovation in advanced semiconductor technologies.

The MoU was signed on behalf of IIT Bhubaneswar by Prof. Vijay Shankar Pasupureddi, Dean, Sponsored Research and Industrial Consultancy (SRIC), and on behalf of Banashree Semiconductors by Dr. Chandramohan Umapathy, Director, in the presence of Prof. Shreepad Karmalkar, Director, IIT Bhubaneswar. Dr. Navjeet Bagga, Assistant Professor and Dr. Sandeep Kumar Samal, Assistant Professor of the School of Electrical and Computer Sciences were present on the occasion.

It may be mentioned that Banashree Semiconductors is engaged in advanced silicon-node, low-power chip design and optimisation of power, performance and area (PPA), with a focus on reduced transistor-count (TAT) architectures for system-on-chip (SoC) applications. The collaboration brings together IIT Bhubaneswar's academic and research capabilities with Banashree Semiconductors' expertise in advanced silicon-node, low-power chip design and optimisation of power, performance and area (PPA), including reduced transistor-count architectures for system-on-chip (SoC) applications.

Under the five-year MoU, the two organisations will undertake joint R&D projects, development of design optimisation flows, faculty and student exchanges, internships, training and collaborative research. They will also explore externally funded research and consultancy projects and facilitate shared use of computing resources, design flows, laboratory facilities, testing infrastructure and research equipment.

A key focus of the partnership will be industry exposure and hands-on opportunities for students. IIT Bhubaneswar students may undertake internships, project work, training and research at Banashree Semiconductors, while the company's engineers and scientists may participate in collaborative research and academic activities at the Institute. The partnership also envisages joint workshops, seminars, short-term courses, conferences and research publications.

The partnership is expected to strengthen the interface between academic research and semiconductor industry requirements, while contributing to skilled manpower development and industry-relevant research in semiconductor design and allied technologies.

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Media/Publication	IBGNews.com		
Date	5th August 2026	Language	English
Headline	IIT Bhubaneswar, Banashree Semiconductors Forge Five-Year Partnership to Accelerate India's Semiconductor Ecosystem		
Link	https://ibgnews.com/2026/08/05/iit-bhubaneswar-banashree-semiconductors-forge-five-year-partnership-to-accelerate-indias-semiconductor-ecosystem/		

Bhubaneswar, August 5: In a significant step towards strengthening India's semiconductor research and innovation ecosystem, the **Indian Institute of Technology (IIT) Bhubaneswar** has entered into a five-year Memorandum of Understanding (MoU) with **Banashree Semiconductors** to promote collaborative research, industry-oriented education, skill development and advanced semiconductor technology initiatives.

The agreement was signed on behalf of IIT Bhubaneswar by **Prof. Vijay Shankar Pasupureddi**, Dean, Sponsored Research and Industrial Consultancy (SRIC), while **Dr. Chandramohan Umapathy**, Director of Banashree Semiconductors, signed on behalf of the company. The signing ceremony was held in the presence of **Prof. Shreepad Karmalkar**, Director of IIT Bhubaneswar. Faculty members **Dr. Navjeet Bagga** and **Dr. Sandeep Kumar Samal** from the School of Electrical and Computer Sciences also attended the event.

Banashree Semiconductors specialises in advanced silicon-node, low-power chip design and optimisation of **power, performance and area (PPA)**, with a particular emphasis on reduced transistor-count architectures for **system-on-chip (SoC)** applications. Through this collaboration, the company's industrial expertise will be combined with IIT Bhubaneswar's academic and research capabilities to advance innovation in semiconductor design.

Under the five-year partnership, both organisations will jointly undertake research and development projects, develop semiconductor design optimisation flows, facilitate faculty and student exchanges, and offer internships, training programmes and collaborative research opportunities. The collaboration will

also explore externally funded research and consultancy projects while enabling shared access to computing resources, laboratory facilities, testing infrastructure, design tools and research equipment.

A major focus of the MoU is to provide students with practical industry exposure. IIT Bhubaneswar students will have opportunities to undertake internships, project work, training and research at Banashree Semiconductors. At the same time, the company's engineers and scientists will contribute to academic and research activities at the institute. The partnership also includes plans for organising joint workshops, seminars, conferences, short-term courses and collaborative research publications.

The initiative is expected to strengthen the interface between academia and industry by aligning advanced research with the evolving needs of India's semiconductor sector. It also aims to contribute to the development of highly skilled manpower and industry-relevant research, supporting the country's growing ambitions in semiconductor design and allied technologies.

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Media/Publication	Pragativadi.com		
Date	5th August 2026	Language	English
Headline	IIT Bhubaneswar Signs MoU with Banashree Semiconductors to Advance Chip Design Research		
Link	https://pragativadi.com/iit-bhubaneswar-signs-mou-with-banashree-semiconductors-to-advance-chip-design-research/		

In a major step toward strengthening academia–industry collaboration in India's semiconductor ecosystem, the Indian Institute of Technology (IIT) Bhubaneswar has signed a Memorandum of Understanding (MoU) with Banashree Semiconductors to advance joint research, innovation, and skill development in next-generation chip technologies.

The MoU was signed by Prof. Vijay Shankar Pasupureddi, Dean (SRIC), IIT Bhubaneswar, and Dr Chandramohan Umapathy, Director of Banashree Semiconductors, in the presence of Prof. Shreepad Karmalkar, Director of IIT Bhubaneswar. Faculty members Dr. Navjeet Bagga and Dr. Sandeep Kumar Samal from the School of Electrical and Computer Sciences also attended the signing ceremony.

Banashree Semiconductors specialises in advanced silicon-node, low-power chip design and optimisation of power, performance, and area (PPA), focusing on reduced transistor-count architectures for system-on-chip (SoC) applications. The collaboration will combine IIT Bhubaneswar's academic and research strengths with Banashree's industry expertise to drive innovation in semiconductor design and manufacturing.

Under the five-year agreement, both institutions will undertake joint R&D projects, develop design-optimisation flows, and facilitate faculty and student exchanges, internships, and training programmes. They will also explore externally funded research, consultancy projects, and shared use of laboratories, testing infrastructure, and computing resources.

A key focus will be hands-on industry exposure for students. IIT Bhubaneswar students will gain opportunities for internships and project work at Banashree Semiconductors, while the company's engineers will participate in collaborative research and academic activities at the institute.

The partnership aims to bridge the gap between academic research and industry needs, nurturing skilled manpower and fostering innovation in semiconductor design, fabrication, and allied technologies.

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Media/Publication	OdishaStand.com		
Date	5th August 2026	Language	English
Headline	IIT Bhubaneswar and Banashree Semiconductors Join Forces to Drive Advanced Semiconductor Innovation		
Link	https://odishastand.com/iit-bhubaneswar-banashree-semiconductors-mou-chip-design-research		

In a strategic move to strengthen academia-industry collaboration within the semiconductor sector, the Indian Institute of Technology (IIT) Bhubaneswar has signed a Memorandum of Understanding (MoU) with Banashree Semiconductors. This partnership aims to advance joint research, industry-oriented education, professional training and innovation in advanced semiconductor technologies.

The agreement was officially signed on behalf of IIT Bhubaneswar by Prof. Vijay Shankar Pasupureddi, Dean of Sponsored Research and Industrial Consultancy and on behalf of Banashree Semiconductors by Dr. Chandramohan Umapathy, Director. The signing took place in the presence of Prof. Shreepad Karmalkar, Director of IIT Bhubaneswar, alongside key academic representatives including Dr. Navjeet Bagga and Dr. Sandeep Kumar Samal, both Assistant Professors at the School of Electrical and Computer Sciences.

Banashree Semiconductors specializes in advanced silicon-node, low-power chip design and the optimization of power, performance and area (PPA), with a core emphasis on reduced transistor-count architectures for system-on-chip applications. This collaborative effort effectively bridges IIT Bhubaneswar's extensive academic and research capabilities with Banashree Semiconductors' industrial expertise in low-power chip architectures and design optimization.

Frameworked as a five-year initiative, the two organizations will collaborate on joint research and development projects, the creation of specialized design optimization flows, faculty and student exchange programs, structured internships and skill training. Additionally, both entities plan to pursue externally funded research and consultancy assignments while facilitating the shared

utilization of computing resources, proprietary design flows, laboratory equipment, testing infrastructure and research facilities.

A primary pillar of this cooperation is delivering practical industry exposure and hands-on learning experiences for academic talent. IIT Bhubaneswar students will have opportunities to complete internships, project work and research at Banashree Semiconductors. Concurrently, engineers and scientists from Banashree Semiconductors will engage in academic activities and collaborative research at the institute. The roadmap also encompasses joint workshops, technical seminars, short-term courses, academic conferences and co-authored research publications.

Ultimately, this partnership is designed to align academic research outcomes directly with the evolving demands of the semiconductor market. By fostering skilled manpower development and supporting industry-relevant innovation, the collaboration aims to make significant contributions to the national ecosystem for semiconductor design and allied technologies.

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Media/Publication	OdishaHaat.com		
Date	5th August 2026	Language	English
Headline	IIT Bhubaneswar Partners with Banashree Semiconductors to Boost Next-Gen Semiconductor R&D		
Link	https://odishahaat.com/iit-bhubaneswar-partners-banashree-semiconductors-next-gen-chip-design		

The Indian Institute of Technology (IIT) Bhubaneswar has entered into a Memorandum of Understanding (MoU) with Banashree Semiconductors to accelerate joint research, industry-focused education and technology innovation in the semiconductor domain. The strategic partnership bridges higher education with industrial expertise to address critical developments in modern microelectronics.

The formal signing featured Prof. Vijay Shankar Pasupureddi, Dean of Sponsored Research and Industrial Consultancy (SRIC) for IIT Bhubaneswar and Dr. Chandramohan Umapathy, Director of Banashree Semiconductors. The ceremony was held in the presence of Prof. Shreepad Karmalkar, Director of IIT Bhubaneswar, along with Dr. Navjeet Bagga and Dr. Sandeep Kumar Samal, Assistant Professors from the School of Electrical and Computer Sciences.

Banashree Semiconductors brings specialized capabilities in advanced silicon-node, low-power chip design, focusing on power, performance and area optimization alongside reduced transistor-count architectures for system-on-chip integration. By combining these industrial methodologies with IIT Bhubaneswar's academic depth, the collaboration establishes a comprehensive platform for technical advancement.

Spanning a five-year duration, the agreement covers joint research projects, design flow development, exchange programs for faculty and students and targeted training programs. Both partners will also seek external research funding, undertake consultancy work and share key infrastructure including computing systems, laboratories, testing environments and specialized research tools.

Student development forms a major component of the initiative. IIT Bhubaneswar students will gain access to practical internships, research projects and direct industry exposure at Banashree Semiconductors. In turn, technical personnel from Banashree Semiconductors will participate in research and instruction at the institute. The two entities will further host joint workshops, short courses, seminars and academic conferences while co-publishing research findings.

This initiative seeks to align academic research with commercial semiconductor requirements, developing high-skilled technical talent and driving research applicable to real-world chip design and emerging electronics infrastructure.

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Media/Publication	YuvaSamaya.com		
Date	5th August 2026	Language	Odia
Headline	IIT Bhubaneswar Partners with Banashree Semiconductors to Boost Next-Gen Semiconductor R&D		
Link	https://yuvasamay.com/iit-bhubaneswar-and-banashree-semiconductors-mou-for-semiconductor-research		

ସେମିକଣ୍ଡକ୍ଟର କ୍ଷେତ୍ରରେ ଶିକ୍ଷାନୁଷ୍ଠାନ ଓ ଶିଳ୍ପ ମଧ୍ୟରେ ସହଯୋଗକୁ ଆହୁରି ସୁଦୃଢ଼ କରିବା ଦିଗରେ ଭାରତୀୟ ପ୍ରଯୁକ୍ତି ପ୍ରତିଷ୍ଠାନ (ଆଇଆଇଟି) ଭୁବନେଶ୍ୱର ନିକଟରେ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସ ସହ ଏକ ବୁଝାମଣାପତ୍ର (ଏମ୍ଓୟୁ) ସ୍ୱାକ୍ଷର କରିଛି। ଉନ୍ନତ ସେମିକଣ୍ଡକ୍ଟର ପ୍ରଯୁକ୍ତି କ୍ଷେତ୍ରରେ ମିଳିତ ଗବେଷଣା, ଶିଳ୍ପଭିତ୍ତିକ ଶିକ୍ଷା, ପ୍ରଶିକ୍ଷଣ ଓ ନବସୃଜନକୁ ପ୍ରୋତ୍ସାହନ ଦେବା ଏହି ବୁଝାମଣାପତ୍ରର ମୁଖ୍ୟ ଉଦ୍ଦେଶ୍ୟ।

ଆଇଆଇଟି ଭୁବନେଶ୍ୱର ପକ୍ଷରୁ ପ୍ରାୟୋଜିତ ଗବେଷଣା ଓ ଶିଳ୍ପ ପରାମର୍ଶ (ଏସ୍ଆର୍ଆଇସି)ର ଡିନ୍ ପ୍ରଫେସର ବିଜୟ ଶଙ୍କର ପସୁପୁରେଡ୍ଡି ଏବଂ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସ ପକ୍ଷରୁ ନିର୍ଦ୍ଦେଶକ ଡ. ଚନ୍ଦ୍ରମୋହନ ଉମାପତି ଏହି ବୁଝାମଣାପତ୍ରରେ ସ୍ୱାକ୍ଷର କରିଥିଲେ। ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ନିର୍ଦ୍ଦେଶକ ପ୍ରଫେସର ଶ୍ରୀପାଦ କରମଲକରଙ୍କ ଉପସ୍ଥିତିରେ ଏହି ବୁଝାମଣାପତ୍ର ସ୍ୱାକ୍ଷରିତ ହୋଇଥିଲା। ଏହି ଅବସରରେ ସ୍କୁଲ୍ ଅଫ୍ ଇଲେକ୍ଟ୍ରିକାଲ୍ ଆଣ୍ଡ କମ୍ପ୍ୟୁଟର ସାଇନ୍ସେସ୍‌ର ଆସିଷ୍ଟାଣ୍ଟ ପ୍ରଫେସର ଡକ୍ଟର ନବଜିତ ବାଗ୍ଚା ଓ ଡକ୍ଟର ସନ୍ଦୀପ କୁମାର ସାମଲ ଉପସ୍ଥିତ ଥିଲେ।

ଉଲ୍ଲେଖଯୋଗ୍ୟ ଯେ, ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସ ଉନ୍ନତ ସିଲିକନ୍-ନୋଡ୍, କମ୍ ଶକ୍ତି ବ୍ୟବହାରକାରୀ ଚିପ୍ ଡିଜାଇନ୍ ଏବଂ ପାୱାର, ପରଫର୍ମାନ୍ସ ଓ ଏରିଆ ଅପ୍ଟିମାଇଜେସନ୍ କ୍ଷେତ୍ରରେ କାର୍ଯ୍ୟ କରୁଛି। ବିଶେଷକରି ସିଷ୍ଟମ୍-ଅନ୍-ଚିପ୍ ପ୍ରୟୋଗ ପାଇଁ କମ୍ ସଂଖ୍ୟକ ଟ୍ରାଞ୍ଜିଷ୍ଟର ବ୍ୟବହାର କରୁଥିବା ଆର୍କିଟେକ୍ଚର ଉପରେ କମ୍ପାନୀ ଗୁରୁତ୍ୱ ଦେଉଛି। ଏହି ସହଯୋଗ ମାଧ୍ୟମରେ ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ଶିକ୍ଷା ଓ ଗବେଷଣା ଦକ୍ଷତା ସହ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସର ଉନ୍ନତ ଚିପ୍ ଡିଜାଇନ୍ ଓ ପିପିଏ ଅପ୍ଟିମାଇଜେସନ୍ କ୍ଷେତ୍ରର ବୈଷୟିକ ଦକ୍ଷତାକୁ ଯୋଡ଼ାଯିବ।

ପାଞ୍ଚ ବର୍ଷ ପାଇଁ ହୋଇଥିବା ଏହି ବୁଝାମଣାପତ୍ର ଅନୁଯାୟୀ, ଉଭୟ ସଂସ୍ଥା ମିଳିତ ଭାବେ ଗବେଷଣା ଓ ବିକାଶ ପ୍ରକଳ୍ପ, ଡିଜାଇନ୍ ଅପ୍ଟିମାଇଜେସନ୍, ଅଧ୍ୟାପକ ଓ ଛାତ୍ରଛାତ୍ରୀ ବିନିମୟ, ଇଣ୍ଟର୍ନସିପ୍, ପ୍ରଶିକ୍ଷଣ ଏବଂ ମିଳିତ ଗବେଷଣା କାର୍ଯ୍ୟକ୍ରମ ହାତକୁ ନେବେ। ଏହା ସହିତ ବାହ୍ୟ ଅର୍ଥ ସହାୟତାରେ ଗବେଷଣା ଓ ପରାମର୍ଶ ପ୍ରକଳ୍ପ ପାଇଁ ମଧ୍ୟ ଉଭୟ ସଂସ୍ଥା ସୁଯୋଗ ଖୋଜିବେ। କମ୍ପ୍ୟୁଟିଂ ସମ୍ବଳ, ଡିଜାଇନ୍ ସୁବିଧା, ପରୀକ୍ଷାଗାର, ପରୀକ୍ଷଣ ଭିତ୍ତିଭୂମି ଓ ଗବେଷଣା ଉପକରଣର ମିଳିତ ବ୍ୟବହାରକୁ ମଧ୍ୟ ପ୍ରୋତ୍ସାହିତ କରାଯିବ।

ଛାତ୍ରଛାତ୍ରୀଙ୍କୁ ଶିଳ୍ପ କ୍ଷେତ୍ରର ପ୍ରତ୍ୟକ୍ଷ ଅନୁଭୂତି ଓ ବ୍ୟବହାରିକ କାର୍ଯ୍ୟର ସୁଯୋଗ ପ୍ରଦାନ କରିବା ଏହି ସହଭାଗିତାର ଅନ୍ୟତମ ପ୍ରମୁଖ ଦିଗ। ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ଛାତ୍ରଛାତ୍ରୀମାନେ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସରେ ଇଣ୍ଟର୍ନସିପ୍, ପ୍ରକଳ୍ପ କାର୍ଯ୍ୟ, ପ୍ରଶିକ୍ଷଣ ଓ ଗବେଷଣା କରିବାର ସୁଯୋଗ ପାଇବେ। ସେହିପରି କମ୍ପାନୀର ଇଞ୍ଜିନିୟର ଓ ବୈଜ୍ଞାନିକମାନେ ଆଇଆଇଟି ଭୁବନେଶ୍ୱରରେ ମିଳିତ ଗବେଷଣା ଓ ଶିକ୍ଷା ସମ୍ପର୍କିତ କାର୍ଯ୍ୟକ୍ରମରେ ଭାଗ ନେଇପାରିବେ। ଏହା ସହ ମିଳିତ କର୍ମଶାଳା, ସେମିନାର, ସ୍ୱଳ୍ପକାଳୀନ ପାଠ୍ୟକ୍ରମ, ସମ୍ମିଳନୀ ଏବଂ ଗବେଷଣା ପ୍ରକାଶନକୁ ମଧ୍ୟ ପ୍ରୋତ୍ସାହନ ଦିଆଯିବ।

ଏହି ସହଭାଗିତା ଶିକ୍ଷା ଓ ଗବେଷଣା କ୍ଷେତ୍ରକୁ ସେମିକଣ୍ଡକ୍ଟର ଶିଳ୍ପର ଆବଶ୍ୟକତା ସହ ଆହୁରି ନିକଟତର କରିବ ବୋଲି ଆଶା କରାଯାଉଛି। ଏଥିସହ ସେମିକଣ୍ଡକ୍ଟର ଡିଜାଇନ୍ ଓ ଆନୁଷଙ୍ଗିକ ପ୍ରଯୁକ୍ତି କ୍ଷେତ୍ରରେ ଦକ୍ଷ ମାନବ ସମ୍ବଳ ସୃଷ୍ଟି ଏବଂ ଶିଳ୍ପ ଉପଯୋଗୀ ଗବେଷଣାକୁ ମଧ୍ୟ ଏହା ପ୍ରୋତ୍ସାହିତ କରିବ।

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ଭାରତର ସେମିକଣ୍ଡକ୍ଟର ମିଶନ୍‌କୁ ଆଗେଇ ନେବା ଏବଂ ଗବେଷଣା-ଶିଳ୍ପ କ୍ଷେତ୍ର ମଧ୍ୟରେ ଥିବା ବ୍ୟବଧାନକୁ ପୂରଣ କରିବା ପାଇଁ ଭାରତୀୟ ପ୍ରଯୁକ୍ତି ପ୍ରତିଷ୍ଠାନ (ଆଇଆଇଟି) ଭୁବନେଶ୍ୱର ଏବଂ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସ ମଧ୍ୟରେ ଏକ ମହତ୍ୱପୂର୍ଣ୍ଣ ବୁଝାମଣାପତ୍ର (ଏମ୍‌ଓୟୁ) ସ୍ୱାକ୍ଷରିତ ହୋଇଛି। ଉନ୍ନତ ସେମିକଣ୍ଡକ୍ଟର ପ୍ରଯୁକ୍ତିବିଦ୍ୟାରେ ମିଳିତ ଗବେଷଣା, ଶିଳ୍ପ-ଭିତ୍ତିକ ଶିକ୍ଷା, ନବସୃଜନ ଏବଂ ପ୍ରଶିକ୍ଷଣ ପ୍ରସାରିତ କରିବା ଏହି ବୁଝାମଣାପତ୍ରର ମୁଖ୍ୟ ଲକ୍ଷ୍ୟ।

ଏହି ସହଭାଗିତା ପତ୍ରରେ ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ପ୍ରାୟୋଜିତ ଗବେଷଣା ଓ ଶିଳ୍ପ ପରାମର୍ଶ (ଏସ୍‌ଆର୍‌ଆଇସି) ଡିନ୍ ପ୍ରଫେସର ବିଜୟ ଶଙ୍କର ପସୁପୁରେଡ୍ଡି ଏବଂ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସର ନିର୍ଦ୍ଦେଶକ ଡ. ଚନ୍ଦ୍ରମୋହନ ଉମାପତି ଦସ୍ତଖତ କରିଛନ୍ତି। ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ନିର୍ଦ୍ଦେଶକ ପ୍ରଫେସର ଶ୍ରୀପାଦ କରମଲକରଙ୍କ ପ୍ରତ୍ୟକ୍ଷ ଉପସ୍ଥିତିରେ ଏହି କାର୍ଯ୍ୟକ୍ରମ ଅନୁଷ୍ଠିତ ହୋଇଥିଲା। ଏହି ସମାରୋହରେ ସ୍କୁଲ୍ ଅଫ୍ ଇଲେକ୍ଟ୍ରିକାଲ୍ ଆଣ୍ଡ କମ୍ପ୍ୟୁଟର ସାଇନ୍ସେସ୍‌ର ଆସିଷ୍ଟାଣ୍ଟ ପ୍ରଫେସର ଡକ୍ଟର ନବଜିତ ବାଗ୍ଚା ଏବଂ ଡକ୍ଟର ସନ୍ଦୀପ କୁମାର ସାମଲ ମଧ୍ୟ ଭାଗ ନେଇଥିଲେ।

ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସ ମୁଖ୍ୟତଃ ଉନ୍ନତ ସିଲିକନ୍-ନୋଡ୍, କମ୍ ଶକ୍ତି ସଞ୍ଚାଳିତ ଚିପ୍ ଡିଜାଇନ୍, ଏବଂ ପାୱାର, ପରଫର୍ମାନ୍ସ ଓ ଏରିଆ ଅପ୍ଟିମାଇଜେସନ୍‌ରେ ବିଶେଷଜ୍ଞତା ହାସଲ କରିଛି। କମ୍ପାନୀ ସିଷ୍ଟମ୍-ଅନ୍-ଚିପ୍ ପ୍ରୟୋଗ ପାଇଁ କମ୍ ଟ୍ରାଞ୍ଜିଷ୍ଟର ବ୍ୟବହାର ହେଉଥିବା ଆର୍କିଟେକ୍ଚର ନିର୍ମାଣ ଉପରେ କାର୍ଯ୍ୟ କରୁଛି। ଏହି ସହଯୋଗ ମାଧ୍ୟମରେ ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ଗବେଷଣା ପ୍ରତିଭା ଏବଂ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସର ବୈଷୟିକ ଜ୍ଞାନକୌଶଳର ବାସ୍ତବ ସମ୍ମିଶ୍ରଣ ଘଟିବ।

ପାଞ୍ଚ ବର୍ଷୀୟ ଏହି ଚୁକ୍ତିନାମା ଅଧୀନରେ ଉଭୟ ସଂସ୍ଥା ମିଳିତ ଗବେଷଣା ଓ ବିକାଶ ପ୍ରକଳ୍ପ, ଡିଜାଇନ୍ ଅପ୍ଟିମାଇଜେସନ୍, ଛାତ୍ର ଓ ଅଧ୍ୟାପକ ବିନିମୟ, ପ୍ରଶିକ୍ଷଣ ଏବଂ ଇଣ୍ଟର୍ନସିପ୍ କାର୍ଯ୍ୟକ୍ରମ ସମ୍ପାଦନ କରିବେ। ବାହ୍ୟ ଅର୍ଥ ସହାୟତାରେ ପ୍ରକଳ୍ପ ସଞ୍ଚାଳନ ସହ କମ୍ପ୍ୟୁଟିଂ ସମ୍ବଳ, ଡିଜାଇନ୍ ସୁବିଧା, ପରୀକ୍ଷାଗାର ଏବଂ ଲାବୋରେଟୋରୀ ଭିତ୍ତିଭୂମିର ସାଝା ବ୍ୟବହାର କରାଯିବ।

ଏହି ସହଭାଗିତା ଦ୍ୱାରା ଆଇଆଇଟି ଭୁବନେଶ୍ୱରର ଛାତ୍ରଛାତ୍ରୀମାନେ ବନଶ୍ରୀ ସେମିକଣ୍ଡକ୍ଟର୍ସରେ ଗବେଷଣା, ପ୍ରଶିକ୍ଷଣ ଓ ଇଣ୍ଟର୍ନସିପ୍‌ର ବ୍ୟାପକ ସୁଯୋଗ ପାଇପାରିବେ। ସେହିପରି କମ୍ପାନୀର ବୈଜ୍ଞାନିକ ଓ ଇଞ୍ଜିନିୟରମାନେ ଶିକ୍ଷାନୁଷ୍ଠାନର ଗବେଷଣା କାର୍ଯ୍ୟକ୍ରମ, ସେମିନାର, କର୍ମଶାଳା, ସମ୍ମିଳନୀ ଏବଂ ପାଠ୍ୟକ୍ରମରେ ଅଂଶଗ୍ରହଣ କରିପାରିବେ। ଏହି ପଦକ୍ଷେପ ସେମିକଣ୍ଡକ୍ଟର ଶିଳ୍ପ ପାଇଁ ଆବଶ୍ୟକୀୟ ଦକ୍ଷ ମାନବ ସମ୍ବଳ ପ୍ରସ୍ତୁତ କରିବାରେ ବେଶ୍ ସହାୟକ ହେବ।