

Department of Electrical Engineering IIT Bhubaneswar

List of candidates shortlisted for technical interview round towards admission into PhD Programme in Electrical Engineering Discipline for Autumn 2026-27 (Round II) academic session

Interview	Venue
Date of Interview: 21st August 2026 (Friday) Reporting time for document verification: 11:00 am Interviews start after document verification in the department	School of Electrical and Computer Sciences IIT Bhubaneswar, Argul, Pin 752050

Sl. No.	Application no.	Name of Applicant	Gender	Category	Type of admission
1	2026151010121	DILIP JHA	male	GE	WP
2	2026151110275	MAITREE	female	OB	IS
3	2026151110297	JITENDRA KUMAR SAHOO	male	OB	IS
4	2026151110416	SURYAMANI SAHOO	male	GE	WP
5	2026151111202	GOLLAPALLI MANIKANTA	male	EWS	IS
6	2026151210185	RAJ ANAND	male	OB	IS
7	2026151210321	ASIMA SABAT	female	GE	IS
8	2026151310254	ASHIT KHADIRATNA	male	GE	IS
9	2026151311101	KANAPARTI SUDARSANREDDY	male	GE	WP
10	2026151510860	SARBANI MOHANTY	female	GE	IS
11	2026151610380	BISWAJIT SWAIN	male	OB	IS
12	2026151610656	RANESH S	male	GE	IS
13	2026151710172	KHUSHBOO KUMARI	female	SC	IS
14	2026151710617	NAOREM RUDRA	male	OB	IS
15	2026151810521	ABHAY KUMAR HIMANSHU	male	OB	IS
16	2026151810745	SUVRODEB SARKAR	male	SC	IS
17	2026151810749	AKASH ROY CHOUDHURY	male	GE	IS
18	2026151910948	SAMBHUNATH MOHANTA	male	OB	IS